

# PATENT ABSTRACTS OF JAPAN

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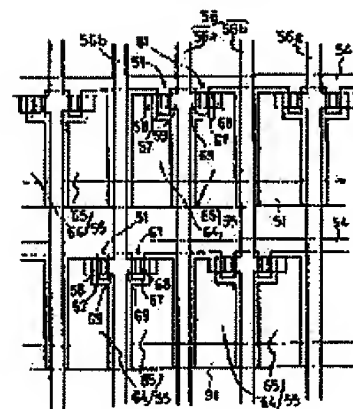
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(54) ACTIVE MATRIX DISPLAY DEVICE

(57)Abstract:

**PURPOSE:** To eliminate flicker caused by the difference of the parasitic capacity of a TFT by symmetrically providing respective gate electrodes of a pair of thin film transistors each other and at right angle in a direction in which a gate bus wiring is extended and forming respective drain electrodes in a symmetrical shape each other.

**CONSTITUTION:** The gate electrodes 57 and 67 of the pair of TFTs 51 and 61 respectively connected to the pair of split electrodes 64 and 65 constituting a picture element electrode 55 are perpendicularly provided in the direction in which the gate bus wiring 54 is extended. And the gate electrodes 57 and 67, and the drain electrodes 58 and 68 are respectively formed in the symmetrical shape each other with respect to a surface positioned in a center between the TFTs 51 and 61 which are pairing up. Consequently, the total amount parasitic capacity of the pair of TFTs connected to the picture element electrode 55 is constant with respect to the respective picture element electrode 55 even though the positional deviation of the drain electrodes 58 and 68 is



caused. Thus, the flicker caused by the difference of the parasitic capacity is eliminated.

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## LEGAL STATUS

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